



JPW

Docket No.: M4065.0960/P960
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Alexander Krymski

Application No.: 10/705,843

Art Unit: 2878

Filed: November 13, 2003

Examiner: Thanh X. Luu

For: PIXEL SIGNAL BINNING AND
INTERPOLATION IN COLUMN
CIRCUITS OF A SENSOR CIRCUIT

STATUS INQUIRY

U.S. Patent and Trademark Office
Customer Window
Randolph Building
401 Dulany Street
Alexandria, VA 22314

Dear Sir:

It is respectfully requested that the attorneys named below be advised of the status of the above-captioned application. Please advise when we might expect to receive an Office Action from the Patent and Trademark Office.

Dated: September 21, 2005

Respectfully submitted,

By 

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